

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Not for submission under 37 CFR 1.99)</i>	Application Number	09488942
	Filing Date	2000-01-21
	First Named Inventor	Paul W. SHERER
	Art Unit	2152
	Examiner Name	Sean M. REILLY
	Attorney Docket Number	1112.US.CIP

U.S.PATENTS						
Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	4138723		1979-06-06		
	2	4500990		1985-02-19		
	3	4754399		1988-06-28		
	4	4823312		1989-04-18		
	5	4860183		1989-08-22		
	6	4907225		1990-03-06		
	7	4949333		1990-08-14		
	8	4969164		1990-11-06		

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	09488942
Filing Date	2000-01-21
First Named Inventor	Paul W. SHERER
Art Unit	2152
Examiner Name	Sean M. REILLY
Attorney Docket Number	1112.US.CIP

9	5048012	1991-09-10
10	5088091	1992-02-11
11	5121479	1992-06-09
12	5179709	1993-01-12
13	5210740	1992-05-11
14	5249271	1993-09-28
15	5307345	1994-04-26
16	5390299	1995-02-14
17	5412782	1995-07-02
18	5488724	1996-01-30
19	5593281	1996-06-06

INFORMATION DISCLOSURE STATEMENT BY APPLICANT <small>(Not for submission under 37 CFR 1.99)</small>	Application Number		09488942	
	Filing Date		2000-01-21	
	First Named Inventor		Paul W. SHERER	
	Art Unit		2152	
	Examiner Name		Sean M. REILLY	
	Attorney Docket Number		1112.US.CIP	

If you wish to add additional U.S. Patent citation information please click the Add button.

U.S.PATENT APPLICATION PUBLICATIONS

Examiner Initial*	Cite No	Publication Number	Kind Code ¹	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1					

If you wish to add additional U.S. Published Application citation information please click the Add button.

FOREIGN PATENT DOCUMENTS

Examiner Initial*	Cite No	Foreign Document Number ³	Country Code ²	Kind Code ⁴	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear	T ⁵
	1							<input type="checkbox"/>

If you wish to add additional Foreign Patent Document citation information please click the Add button

NON-PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.						T ⁵
<i>AS</i>	1	NATIONAL SEMICONDUCTOR, NS 16550A Universal Asynchronous Receiver/Transmitter with FIFOs, 1988.						<input type="checkbox"/>
<i>AS</i>	2	KANAKIA, HEMART and DAVID CHERITON, "The VMP Network Adapter Board (NAB): High-Performance Network Communication for Multiprocessors", ACM, 1988.						<input type="checkbox"/>
<i>AS</i>	3	FUJITSU, LTD, Data sheet for "FUJITSU MB86950 Etherstar Ethernet Controller", December 1989.						<input type="checkbox"/>
<i>AS</i>	4	TEXAS INSTRUMENTS, TL16C550A Asynchronous Communications Element, March 1996.						<input type="checkbox"/>

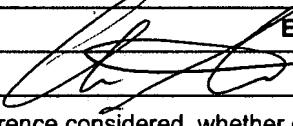
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Not for submission under 37 CFR 1.99)</i>		Application Number	09488942
		Filing Date	2000-01-21
		First Named Inventor	Paul W. SHERER
		Art Unit	2152
		Examiner Name	Sean M. REILLY
		Attorney Docket Number	1112.US.CIP

<i>AS</i>	5	FRAZIER, GREGORY L and YUVAL TARIAR, "The Design and Implementation of a Multi-Queue Buffer for VLSI Communication Switches", IEEE, 1989.	<input type="checkbox"/>
			<input type="checkbox"/>
	6	INTEL, CORP, Datasheet for 82596CA High-Performance 32-Bit Local Area Network Coprocessor", November 1989.	<input type="checkbox"/>
			<input type="checkbox"/>
	7	NGAI, JOHN L and CHARLES L. SEITZ, "A Framework for Adaptive Routing in Multicomputer Networks", ACM, 1989.	<input type="checkbox"/>
			<input type="checkbox"/>
	8	SCHRODER, MICHEAL D et al, "Autonet: A High-Speed, Self-Configuring Local Area Network Using Point-to-Point Links", PRINCIPALS OF COMPUTING SYSTEMS, April 21, 1990.	<input type="checkbox"/>
			<input type="checkbox"/>
	9	GANSSLE, JACK G., "Speed Kills", EMBEDDED SYSTEMS PROGRAMMING, August 1990.	<input type="checkbox"/>
			<input type="checkbox"/>
	10	NATIONAL SEMICONDUCTOR, Data sheet for DP8390C/NS32490C chip, "Data Communications Local Area Network UARTS", 1990.	<input type="checkbox"/>
			<input type="checkbox"/>
	11	NATIONAL SEMICONDUCTOR, Data sheet for "DP 83265 BSI Device (FDDI System Interface)", February 1991.	<input type="checkbox"/>
			<input type="checkbox"/>
	12	KANAKIA, HR, Thesis, "High Performance Host Interfacing for Packet-Switched Networks", Stanford University Department of Computer Science, July 1991.	<input type="checkbox"/>
			<input type="checkbox"/>
	13	ADVANCED MICRO DEVICES, Data sheet for "The SUPERNET 2 Family for FDDI", October 1991.	<input type="checkbox"/>
			<input type="checkbox"/>
	14	SCHRODER, MICHEAL D, et al, "Autonet: A High-Speed, Self-Configuring Local Area Network Using Point-to-Point Links", IEEE Journal on Selected Areas of Communications, October 1991.	<input type="checkbox"/>
			<input type="checkbox"/>
<i>AS</i>	15	OWICKI, SUSAN and ANNA KARLIN, "Factors in the Performance of the ANI Computer Network", ACM SIGMETRICS and PERFORMANCE, June 1992.	<input type="checkbox"/>
			<input type="checkbox"/>

INFORMATION DISCLOSURE STATEMENT BY APPLICANT <small>(Not for submission under 37 CFR 1.99)</small>	Application Number	09488942
	Filing Date	2000-01-21
	First Named Inventor	Paul W. SHERER
	Art Unit	2152
	Examiner Name	Sean M. REILLY
	Attorney Docket Number	1112.US.CIP

	16	MOTOROLA, "FDDI System Interface Users Manual, MC68839", 1994.	<input type="checkbox"/>

If you wish to add additional non-patent literature document citation information please click the Add button

		EXAMINER SIGNATURE	
Examiner Signature		Date Considered	11/6/07
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant.			
<small> ¹ See Kind Codes of USPTO Patent Documents at www.USPTO.GOV or MPEP 901.04. ² Enter office that issued the document, by the two-letter code (WIPO Standard ST.3). ³ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁴ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁵ Applicant is to place a check mark here if English language translation is attached. </small>			